

52973 U.S. PTO  
10/079447  
02/20/02  
Jcw # 912

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10079447	FILING DATE 02/20/2002	CLASS 327	SUBCLASS 4	GAU 2816	EXAMINER LEE
----------------------	---------------------------	--------------	---------------	-------------	-----------------

\*\*APPLICANTS: Gabara Thaddeus; Martin Samuel;

\*\*CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/270,263 02/21/2001

\*\* FOREIGN APPLICATIONS VERIFIED

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO 290-051us

TITLE : Method for modeling noise emitted by digital circuits

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)